

RS flip-flop with a single feedback loop consists of NOT, AND, and OR logic gates, and features asymmetry and a single feedback loop, where the first input is the S input, connected to the first input of the OR gate; the second input is the R input, connected via a NOT gate to the first input of the AND gate, whose second input is connected to the output of the OR gate; and the output is the flip-flop output and is connected to the second input of the OR gate.